

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Suaya and Gabillet

Application No. 09/385,666

Filed: August 26, 1999

For: CAPACITANCE MEASUREMENTS
FOR AN INTEGRATED CIRCUIT

Examiner: Not assigned

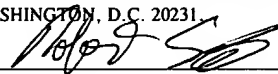
Date: January 23, 2001



Art Unit 2858

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Robert F. Scotti
Attorney for Applicant

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Dear Sir or Madam:

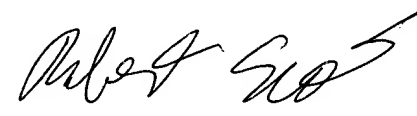
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Respectfully submitted,

By: 
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Date: January 23, 2001